

#Jenny



Finally I get this ebook, thanks for all these I can get now!

#Rio



Cool! I'am really happy

#Markus Jensen



I did not think that this would work, my best friend showed me this website, and it does! I get my most wanted eBook

#Hun Tsu



wtf this great ebook for free?!

#Che Salsa



My friends are so mad that they do not know how I have all the high quality ebook which they do not!

#Diego Butler



so many fake sites. this is the first one which worked! Many thanks

**ITEC 625 Computer Systems  
Architecture Midterm Exam Answers**

**BUY HERE**

<https://www.homeworkmade.com/itec-625-computer-systems-architecture-midterm-exam-answers/>

ITEC 625 Computer Systems Architecture Midterm Exam Answers  
1. For each statement below, Explain Why it is True or False (2 points each, 1 point for correct answer of True or False, and one 1 point for explanation)  
1) Secondary storage is often regarded with main memory in von Neumann architecture.  
2) Program counter stores the address of next instruction to be fetched and instruction in execution.  
3) Address and instruction can be fetched in parallel in von Neumann system by having them as unsigned integer of sign-and-magnitude representation. But there are two representations of 0.  
4) Characteristic of sign-and-magnitude representation is that there are two representations of 0.  
5) Both sign-and-magnitude and two's complement representation use the most significant bit as a sign bit.  
6) When you want to represent 42,000,000,000 in an 8-bit float point number, but the float point system cannot represent it, then it is called a "round-off error".  
7) The order of an unsigned integer is not fixed. It is determined by the programmer.  
8) RISC systems (not only have a small ISA (fewer instructions) but make up for it with better hardware).  
9) All 4-bit bus instructions will generate the response of 4-bit signals to operate the other components.  
10) The parallel architecture allow several instructions to be issued and completed per clock cycle.  
11. Multiple Choice - Choose ONE (1 Answer, 2 points each, 25 possible)  
1. Which of the following sentences about 8-bit two's complement is correct?  
2. The processing required for a single instruction is called an (n) \_\_\_\_\_ cycle.  
3. In \_\_\_\_\_ representation the rule for forming the negative of an integer is to invert the sign bit.  
4. \_\_\_\_\_ is when the result may be larger than can be held in the word size being used.  
5. The 4-bit number base 10 is 1010 in the 4-bit two's complement.  
6. During the \_\_\_\_\_ the Little Endian gives to the final address specified in the instruction the previously fetched.  
7. The \_\_\_\_\_ helps to find instructions in memory and causes them to be executed.  
8. Which of the following sentences about IEEE 754 single precision floating point numbers is correct?  
9. The von Neumann architecture has which of the following features?  
10. The binary string 1011 1010 1111 is equivalent to \_\_\_\_\_.  
11. Short Answer (2 points each, 12 points total)  
1. \*Application is a concept that is used in many areas such as in data memory and in hardware. Use your experience to explain what is meant by virtualization, and what are the benefits and expenses of virtualization.

[Download PDF version of :](#)  
**Advanced Computer Architecture Final Exam Solutions**